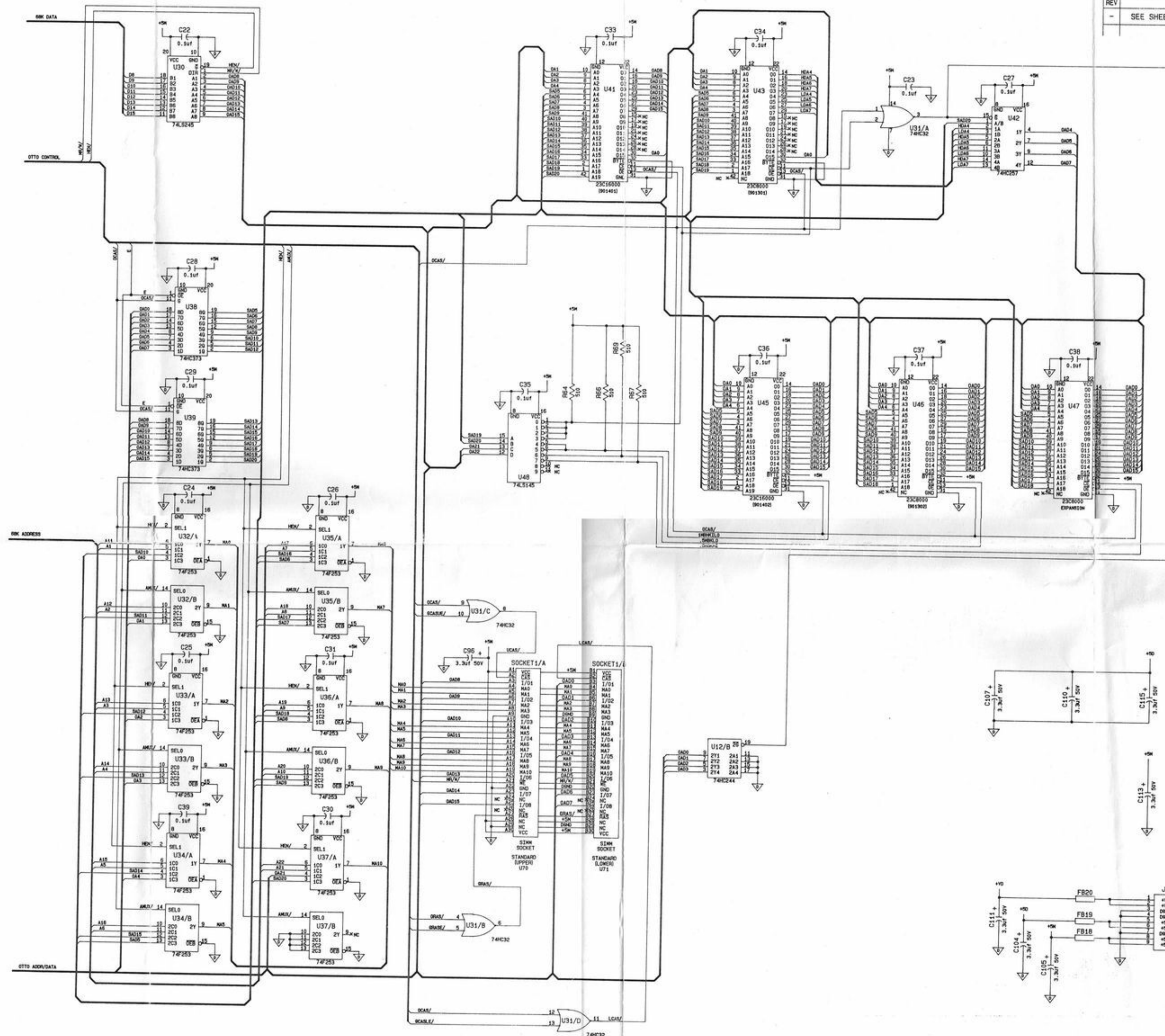
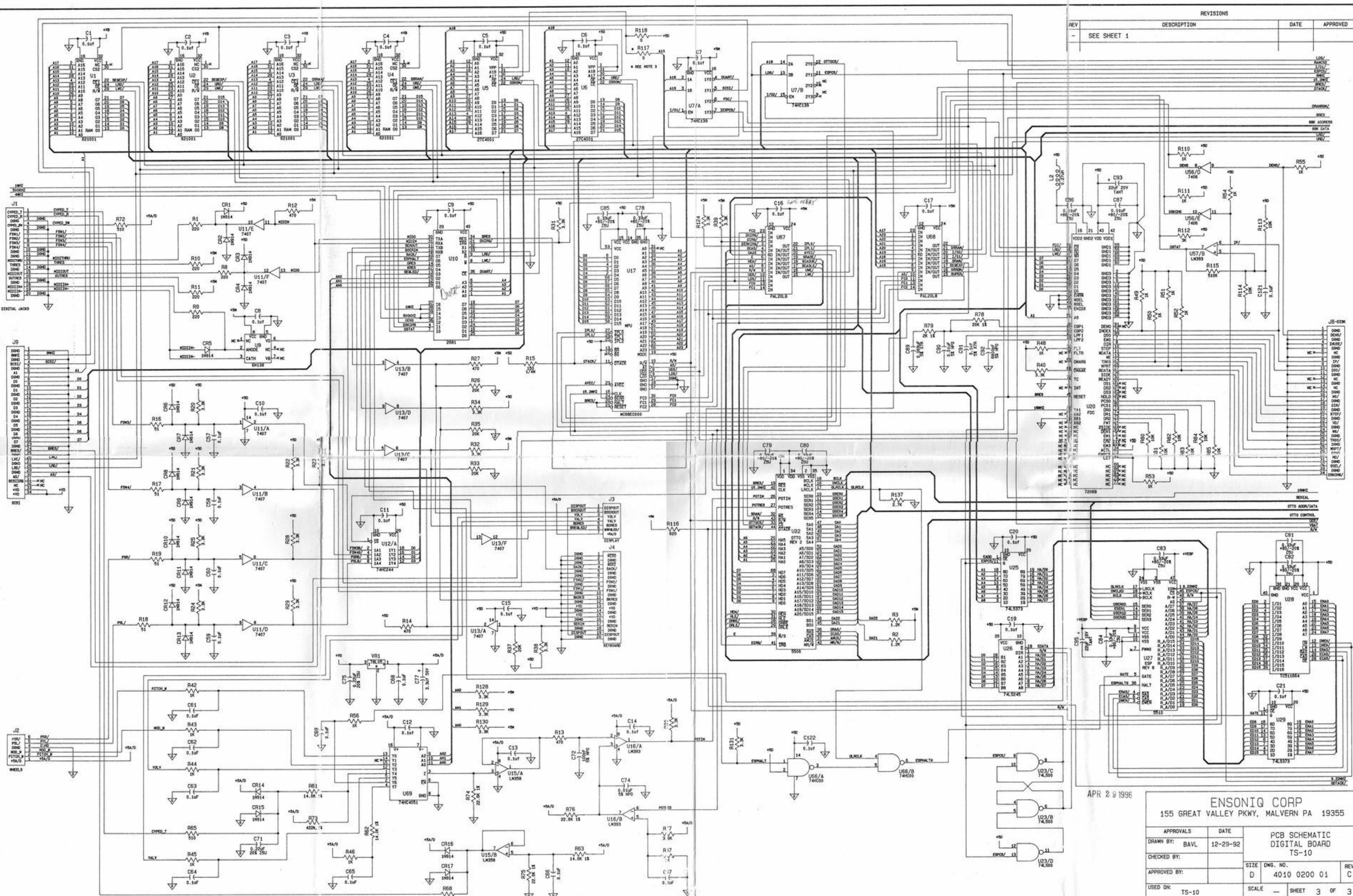




REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
-	SEE SHEET 1		

APR 25 1992

ENSONIQ CORP 155 GREAT VALLEY PKWY, MALVERN PA 19355			
APPROVALS	DATE	PCB SCHEMATIC DIGITAL BOARD TS-10	
DRAWN BY: BAVL	12-29-92		
CHECKED BY:		SIZE DWS. NO.	REV
APPROVED BY:		D 4010020001	C
USED ON:	TS-10	SCALE	SHEET 2 OF 3



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
-	SEE SHEET 1		
1	INITIALS		
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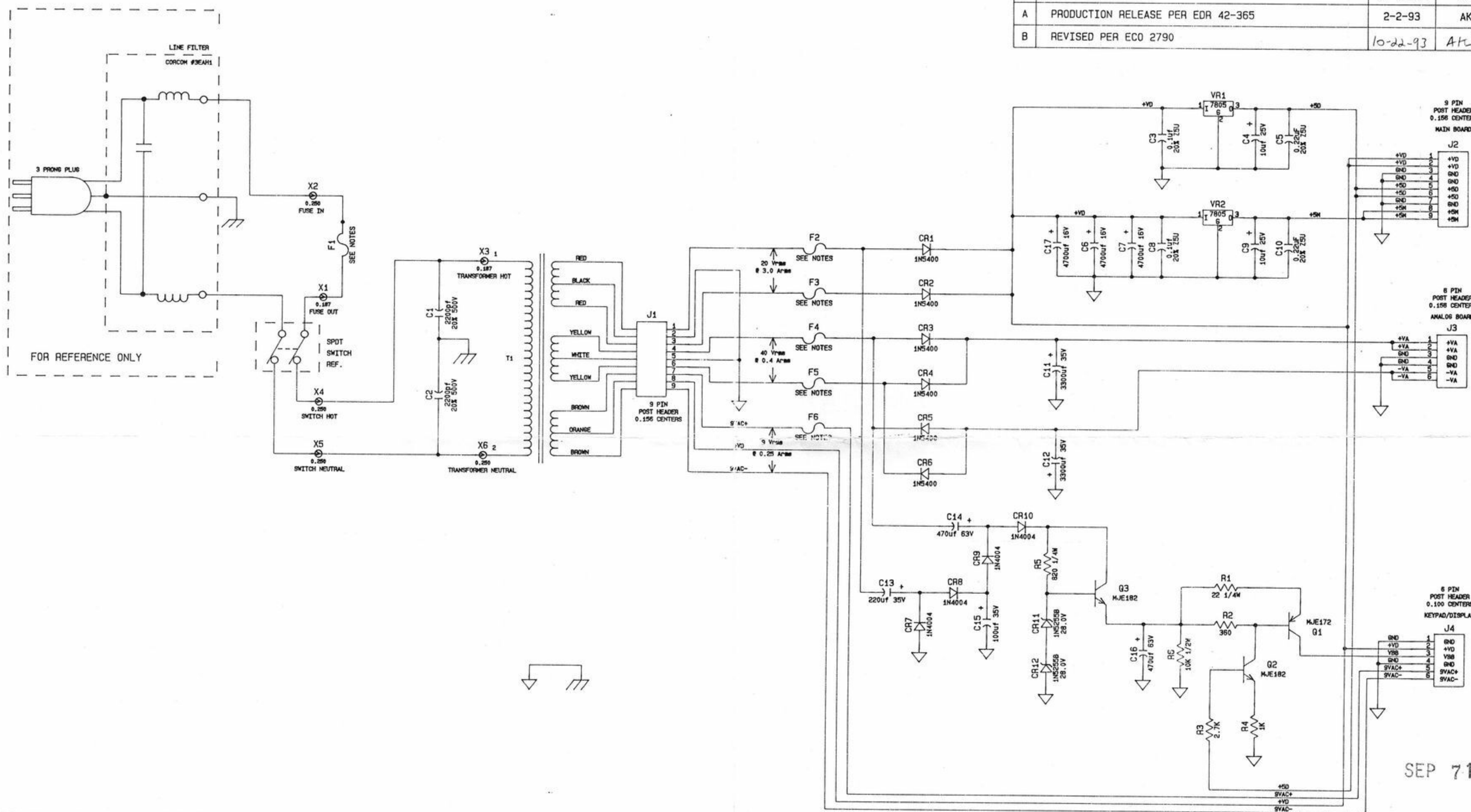
APR 2 9 1996

ENSONIQ CORP
155 GREAT VALLEY PKWY, MALVERN PA 19355

APPROVALS	DATE	PCB SCHEMATIC
DRAWN BY: BAVL	12-29-92	DIGITAL BOARD
CHECKED BY:		TS-10
APPROVED BY:		
USED ON:	TS-10	

SIZE	DWG. NO.	REV
D	4010 0200 01	C
SCALE	SHEET 3 OF 3	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	PROTOTYPE RELEASE		
A	PRODUCTION RELEASE	12-23-92	AKS
A	PRODUCTION RELEASE PER EDR 42-365	2-2-93	AKS
B	REVISED PER ECO 2790	10-22-93	AKS



7. WHEN IEC FUSES ARE USED, THE RATINGS ARE:
 F4 AND F5 F1.6A, F6 F1.6A
 F2 AND F3 F3.15A, F1 F2.0A
6. WHEN UL FUSES ARE USED THE RATINGS ARE:
 F4 AND F5 1.6A, F6 1.6A
 F2 AND F3 3.0A, F1 2.0A
5. ALL FUSES ARE 250V, 5MM X 20MM.
4. X2, X4, X5 AND X6 ARE 0.250 QUICK DISCONNECT LUGS.
3. X1 AND X3 ARE 0.187 QUICK DISCONNECT LUGS.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 16V MINIMUM.
1. ALL RESISTOR VALUES ARE IN OHMS, 1/8W 5%.
- NOTES - UNLESS SPECIFIED OTHERWISE:

SEP 7 1994

ENSONIQ CORP 155 GREAT VALLEY PKWY, MALVERN PA 19355			
APPROVALS	DATE	SCHEMATIC	
DRAWN BY: BAVL	11-24-92	POWER SUPPLY PCB	
CHECKED BY:		TS-10	
APPROVED BY:		SIZE C	DWG. NO. 4010020401
USED ON: TS-10		SCALE	REV B
		SHEET 1 OF 1	

SP4 SCSI interface for Ensoniq TS10/12

